

**EAST Search History**

| <b>Ref #</b> | <b>Hits</b> | <b>Search Query</b>   | <b>DBs</b>  | <b>Default Operator</b> | <b>Plurals</b> | <b>Time Stamp</b>   |
|--------------|-------------|-----------------------|---|-------------------------|----------------|---------------------|
| L6           | 360         | 438/230.<br>ccls.     | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>IBM_TDB | AND                     | ON             | 2009/05/27<br>13:57 |
| L5           | 749         | 257/e21.64.<br>ccls.  | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>IBM_TDB | AND                     | ON             | 2009/05/27<br>13:57 |
| L4           | 515         | 257/e21.626.<br>ccls. | US-PGPUB;<br>USPAT;<br>USOCR; EPO;<br>JPO;<br>IBM_TDB | AND                     | ON             | 2009/05/27<br>13:57 |
| L3           | 398         | 257/392.<br>ccls.     | USPAT   | AND                     | ON             | 2009/05/27<br>13:57 |
| L2           | 345         | 257/391.<br>ccls.     | USPAT   | AND                     | ON             | 2009/05/27<br>13:57 |
| L1           | 1304        | 257/369.<br>ccls.     | USPAT   | AND                     | ON             | 2009/05/27<br>13:57 |

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\ transistors\ 10 709048 CMOS with two spacers and etch stop on one of CMOS.  
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